

Project name

Low voltage laboratory for fuse testing according to Directive 73/23 EEC and European Norm EN 60269-4

Phase no.: 2.

Planned objective: - Achievement of the test circuits
- Documentation of the working mode with the new test circuits

Activity description**Activity II.1** Achievement of the test circuits

The test circuits provide the electric parameters necessary for testing the fuses with voltages up to 100 V and currents from 2.5 A to 2000 A.

The test circuits for the Low voltage laboratory for fuse testing according to European Norm EN 60269-4 were achieved in accordance with the general electric diagram containing the developed electric diagram, apparatus specification, electric board, low voltage source, control desk.

The electric diagram achieved for the Laboratory allows executing the testing diagrams corresponding to each fuse type.

Got results:

Test circuits – 1 pc.

Stage of achieving the planned objective/completion form:

The planned objective was achieved and finalized as “Test circuits for the Low voltage laboratory for fuse testing „.

Activity II.2 Documentation of the working mode with the new test circuits

For documenting the working mode with the new test circuits, the “Technical procedure code PT-LJTFS-01” was drawn up.

The procedure describes the working mode with the test circuits of the Low

voltage laboratory for fuse testing, according to ST ICMET MI no. 13/2006 and complying with the labour safety techniques specific to the low voltage circuits.

It is presented the achievement of the testing diagrams corresponding to each type of low voltage fuse by choosing the corresponding voltage step from the matching transformer (serial or parallel connection of the four transformer secondaries) depending on fuse U_i and U_f , also the fine control in autotransformer; the test current is maintained constant from the automatic control of the autotransformer.

Got results:

Test circuit working procedure code PT-LJTFS-01 – 1 pc.

Stage of achieving the planned objective/completion form:

The planned objective was achieved and finalized as „Procedure– Working mode with test circuits - code PT-LJTFS-01.